

FIG.1  
(PRIOR ART)

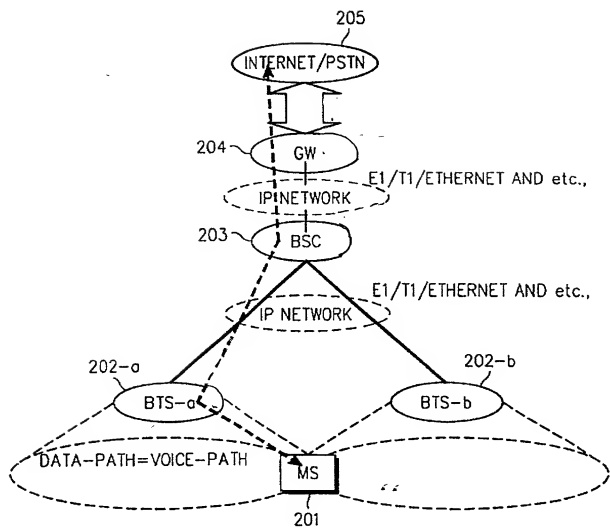


FIG. 2

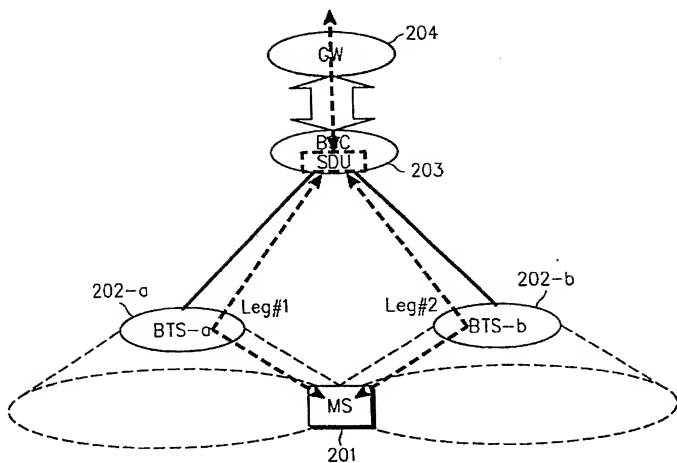


FIG. 3  
(PRIOR ART)

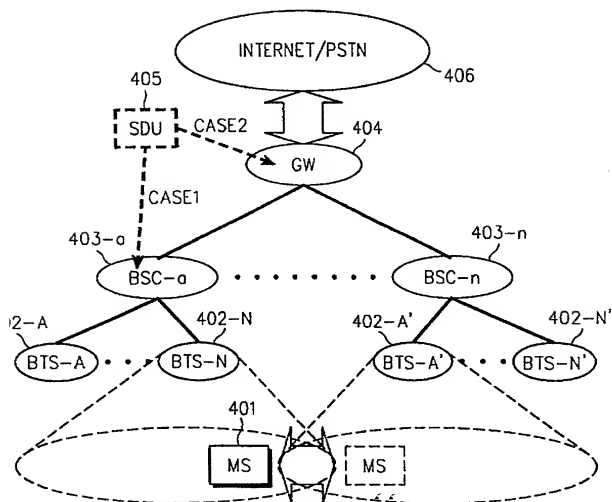


FIG. 4  
(PRIOR ART)

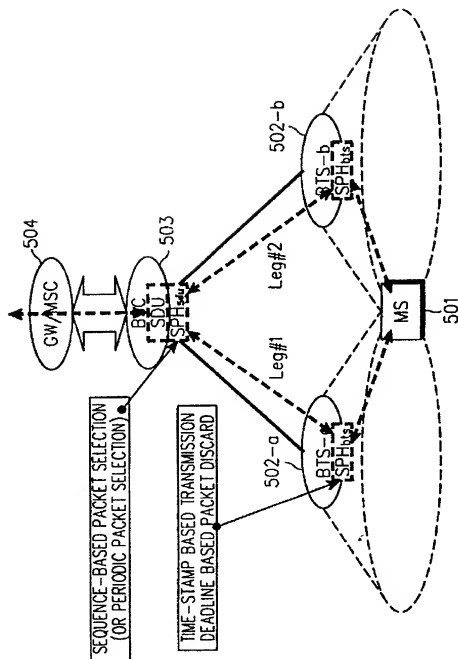


FIG. 5

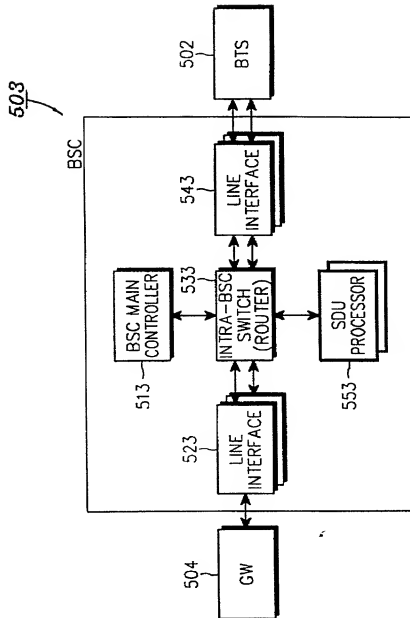


FIG. 6

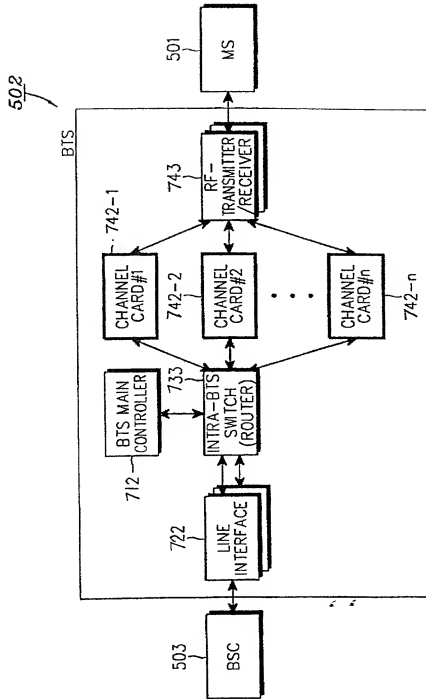


FIG. 7

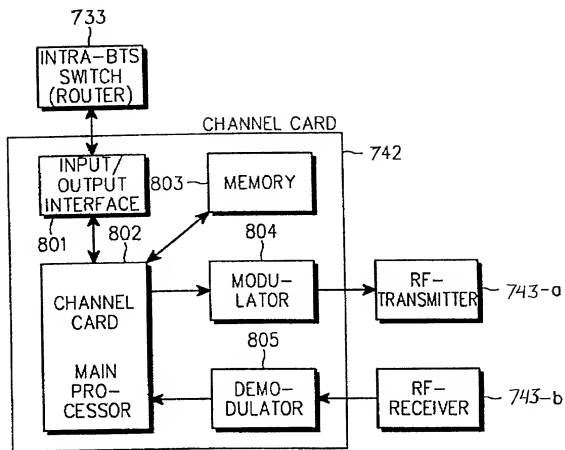


FIG. 8

OPERATION MODE	F1	F2	F3	F4	F5	F6	RSVD
SEQUENCE SUPPORT	SF	SR	RSVD				
	NIL(0x00)						

FIG. 9A

OPERATION MODE	F1	F2	F3	F4	F5	F6	RSVD
SEQUENCE SUPPORT	SF	SR	RSVD				

FIG. 9B

OPERATION MODE	F1	F2	F3	F4	F5	F6	RSVD
SEQUENCE SUPPORT	SF	SR	RSVD				
TRAFFIC PERIOD	TRAFFIC PERIOD						
GAP-TIME	GAP-TIME(OPTIONAL)						

FIG. 9C

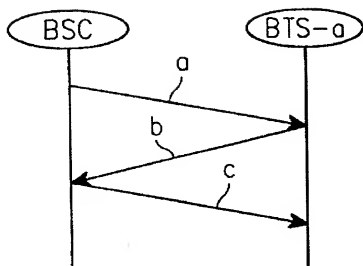


FIG. 10A

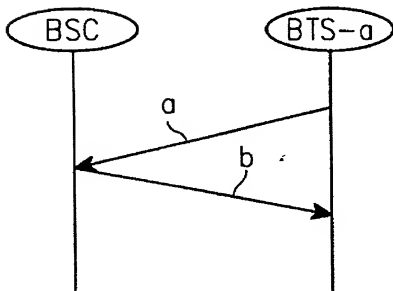


FIG. 10B

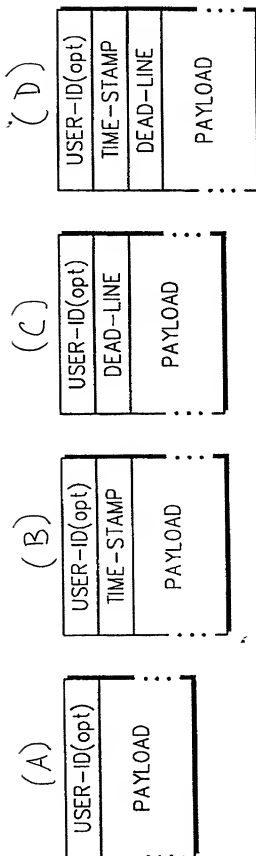


FIG.11

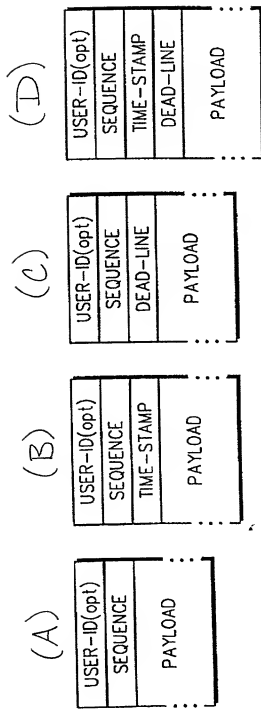


FIG.12

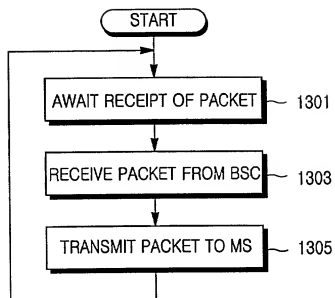


FIG. 13

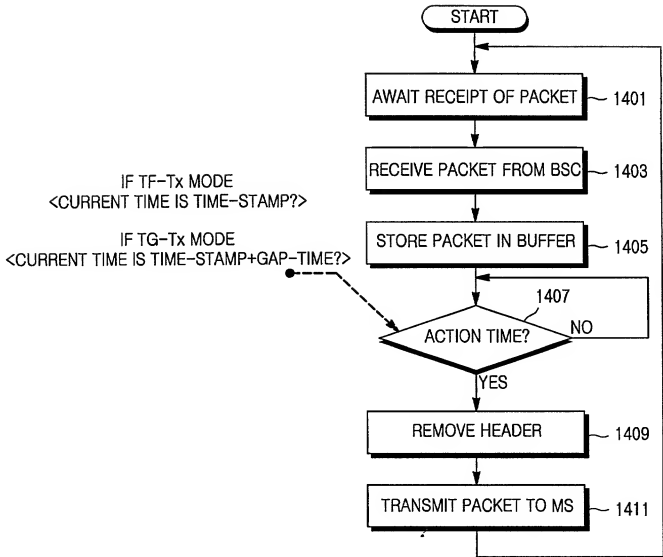


FIG. 14

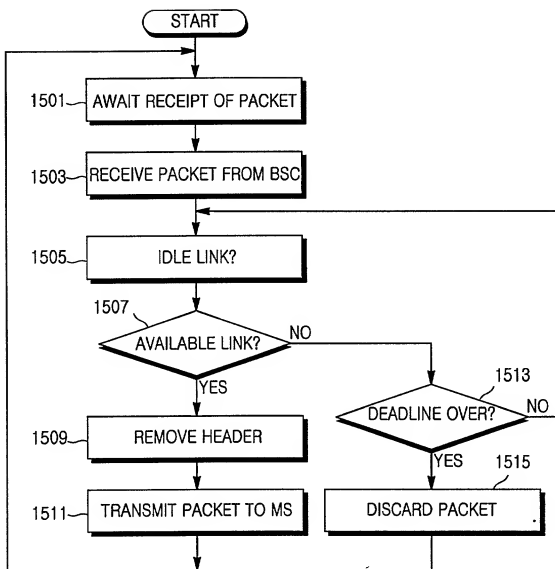
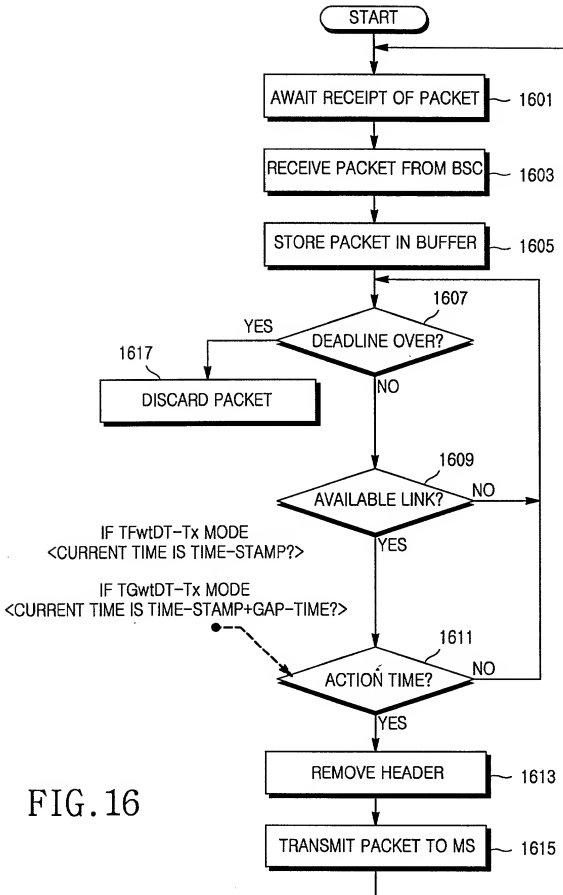


FIG. 15



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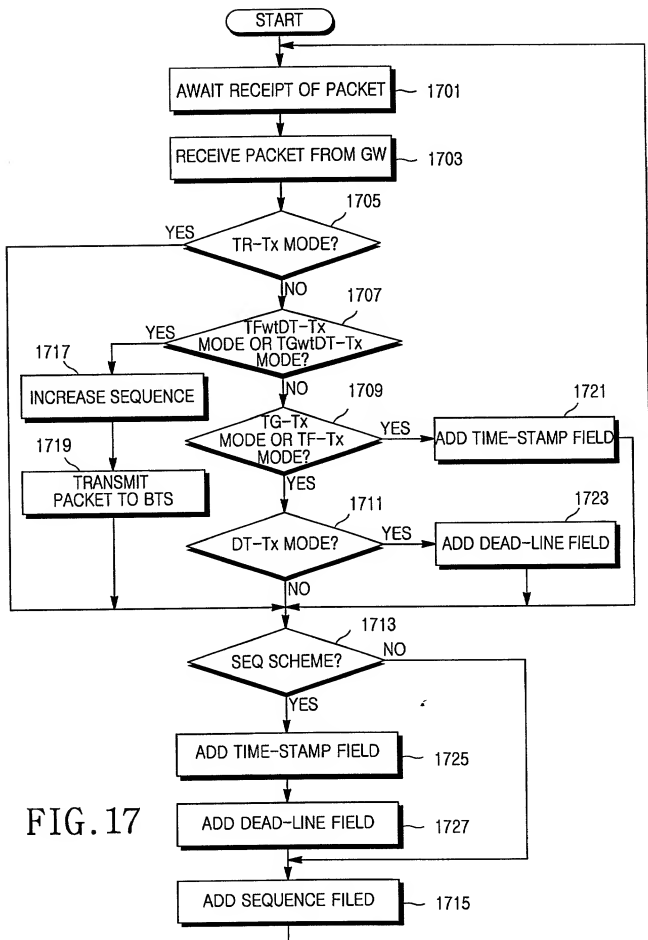


FIG. 17

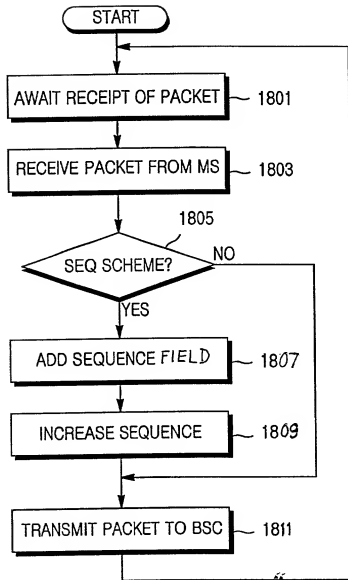


FIG. 18

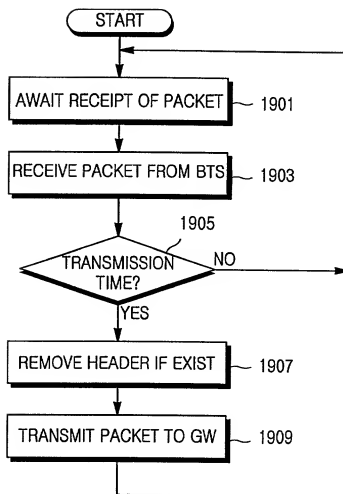


FIG. 19

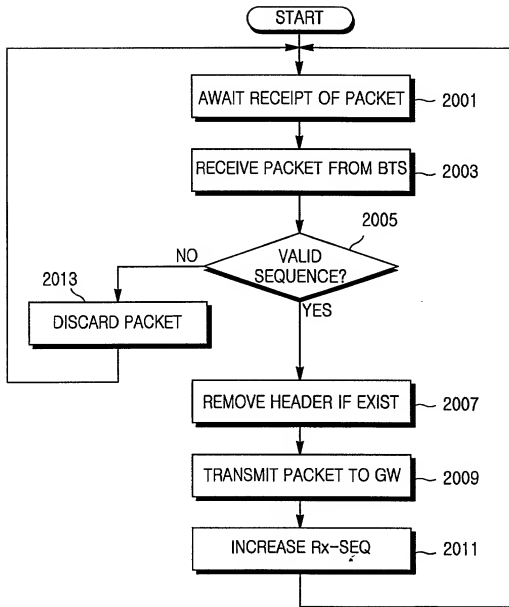


FIG.20